

IN THE SPECIFICATION:

Please amend the specification as set forth below in marked-up form. In accordance with the revised amendment format, a clean copy has been omitted.

Beginning on page 2, line 6:

The quiescent power supply current  $I_{DDQ}$  ~~many~~may include a leakage current flowing even in a defect-free (indefectible) device called ~~as~~"an intrinsic leakage current" or "a normal leakage current") and a defect current occurring due to a defect in the DUT. That is, the quiescent power supply current  $I_{DDQ}$  is defined as the total of the normal leakage current and the defect current.